

Amendments to the Specification:

Please delete the paragraph on page 1, lines 15-16.

Please add the following new paragraphs immediately following the paragraph on page 1, lines 12-13:

Fig. 1 depicts a schematic diagram of one embodiment of a tuner IC with a receiver signal strength indicator.

Fig. 2 depicts a schematic diagram of one embodiment of a modem with a memory outside the tuner IC.

Fig. 3 depicts a schematic diagram of one embodiment of a modem with a memory integrated with the tuner IC.

Please replace the paragraph on page 3, lines 6-9, with the following amended paragraph:

Thus the RF input level can in future accurately be determined from the stored level information in combination with the earlier used gain settings of the digitally controlled amplifiers. The Referring to Fig. 2, the location of the memory 204 can be outside the tuner IC 202 in the modem 200. Referring to Fig. 3, the location of the memory 204 can be , but the possibility of flash memory in future on board of the tuner IC 202 of the modem 200 can be considered as well.